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Technology Center 2100

PATENT
5500-66300/TT4234

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED

Application No.: 09/824,992

Filed: April 2, 2001

Inventor(s):

Kevin J. McGrath

Examiner: Unknown

Group/Art Unit: 2185

Atty. Dkt. No: 5500-66300

JUL 27 2001

Technology Center 2100

Title: Processor Which Overrides
Default Operand Size for
Implicit Stack Pointer
References and Near
Branches

I hereby certify that this correspondence is being deposited with
the United States Postal Service with sufficient postage as first
class mail in an envelope addressed to Commissioner for
Patents, Washington, DC 20231, on the date indicated below.

Lawrence J. Merkel

Printed Name

Signature

July 23, 2001

Date

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Please enter the following preliminary amendment in the above-captioned case.

Please amend the case as listed below.

IN THE SPECIFICATION:

Please insert the following paragraph on page 1, line 1, of the specification:

A¹ This application claims benefit of priority to Provisional Application Serial No.
60/224,368 filed August 9, 2000.

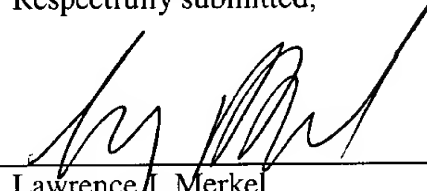
CONCLUSION

If any fees are due, the Commissioner is authorized to charge said fees to Conley, Rose, & Tayon, P.C. Deposit Account No. 501505/5500-66300/LJM.

Also enclosed herewith are the following items:

☒ Return Receipt Postcard

Respectfully submitted,



Lawrence J. Merkel

Reg. No. 41,191

AGENT FOR APPLICANT(S)

Conley, Rose & Tayon, P.C.
P.O. Box 398
Austin, TX 78767-0398
Phone: (512) 476-1400

Date: July 23, 2001